

08/13/97
700
Class
Subclass
ISC/JE CLASSIFICATION
CLASSIFIED 3

6119049

UTILITY SERIAL NUMBER	PATENT DATE SEP 12 1997	PATENT NUMBER 6119049
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SERIAL NUMBER 08/909,489	FILING DATE 08/12/97	CLASS 3200 307	SUBCLASS 46828	GROUP ART UNIT 278614	EXAMINER Pate/n
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CHARLES L. PEDDLE, RENO, NV.

CONTINUING DATA***
VERIFIED PROVISIONAL APPLICATION NO. 60/023,255 08/12/95

FOREIGN APPLICATIONS***
VERIFIED

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Foreign priority claimed 65 USC 119 conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	AS FILED	STATE OR COUNTRY	SHEETS DRWGS.	TOTAL CLAIMS	INDEP. CLAIMS	FILING FEE RECEIVED	ATTORNEYS DOCKET NO.
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TITLE
MEMORY MODULE ASSEMBLY USING PARTIALLY DEFECTIVE CHIPS

U.S. DEPT. OF COMM./ PAT. & TM., PTO-436L (Rev.12-94)

PARTS OF APPLICATION FILED SEPARATELY		Applications Examiner B. Harrison	
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Form PTO-436A
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Issue Fee

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